

# 5/IDS  
PATENT  
Docket No. SPLX.P0099  
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Maki Adeli

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In the application of:

Steven Teig, et al.

Serial No.: 10/066,456

Filing Date: 1/31/02

For: METHOD AND APPARATUS FOR  
PERFORMING TECHNOLOGY  
MAPPING

**INFORMATION DISCLOSURE  
STATEMENT UNDER 37 C.F.R. § 1.97**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

Pursuant to 37 C.F.R. § 1.97 and § 1.98, applicants submit for consideration in the above-identified application the documents listed on the attached Form PTO-1449. Copies of the documents are also submitted herewith. The Examiner is requested to make these documents of record. Applicants would appreciate the Examiner initialing and returning the Form PTO-1449, indicating that the information has been considered and made of record herein.

Submitted in this IDS are copies of a number of applications that have the specifications and drawings similar to the present application. The serial number of each application is written on the first page of each the application specification.

This Information Disclosure Statement is submitted within three months of the application filing date or before the mailing date of a first Office Action on the merits; accordingly, no fee or separate requirements are required.

This Information Disclosure Statement under 37 C.F.R. § 1.97 is not to be construed as a representation that: (i) a complete search has been made; (ii) additional information material to the examination of this application does not exist; (iii) the information, protocols, results and the like reported by third parties are accurate or enabling; or (iv) the above information constitutes prior art to the subject invention.

In the unlikely event that the transmittal letter is separated from this document and the Patent Office determines that an extension and/or other relief is required, applicant petitions for any required relief including extensions of time and authorizes the Assistant Commissioner to charge the cost of such petitions and/or other fees due in connection with the filing of this document to **Deposit Account No. 50-1128** referencing SPLX.P0099. However, the Assistant Commissioner is not authorized to charge the cost of the issue fee to the Deposit Account.

Dated: March 31, 2003

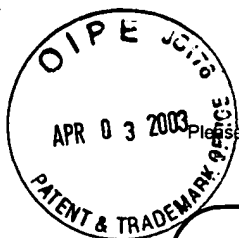
Respectfully submitted,

By: \_\_\_\_\_

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
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<b>TRANSMITTAL FORM</b>  (to be used for all correspondence after initial filing)	Application Number	10/066,456	
	Filing Date	1/31/02	
	First Named Inventor	Steven Teig, et al.	
	Group Art Unit	2825	
	Examiner Name	<none yet>	
Total Number of Pages in This Submission	*	Attorney Docket Number	SPLX.P0099

Enclosures (Check all that apply)		
<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Assignment Papers (for an application)	<input type="checkbox"/> After Allowance Communication to Group
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm Or Individual name	Mani Adeli, Reg. No. 39,585 of Stattler Johansen & Adeli LLP
Signature	
Date	March 31, 2003

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Substitute for form 1449A/PTO			Application Number	10/066,456	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)			Filing Date	1/31/02	
			First Named Inventor	Steven Teig, et al.	
			Group Art Unit	2825	
			Examiner Name		
Sheet	1	of	2	Attorney Docket Number	SPLX.P0099

RELATED U.S. PATENT APPLICATIONS					
Examiner* Initials	Cite No. <sup>1</sup>	U.S. Patent Application Serial Number	Name of Patentee or Applicant of Cited Document	Date of Filing MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	1.	10/062,017	Steven Teig & Asmus Hetzel	01-31-02	
	2.	10/061,459	Steven Teig & Asmuz Hetzel	01-31-02	
	3.	10/062,014	Steven Teig & Asmus Hetzel	01-31-02	
	4.	10/062,044	Steven Teig & Asmuz Hetzel	01-31-02	
	5.	10/066,264	Steven Teig & Asmus Hetzel	01-31-02	
	6.	10/066,188	Steven Teig & Asmus Hetzel	01-31-02	
	7.	10/062,992	Steven Teig & Asmuz Hetzel	01-31-02	
	8.	10/061,474	Steven Teig & Asmus Hetzel	01-31-02	
	9.	10/061,719	Steven Teig & Asmus Hetzel	01-31-02	
	10.	10/062,993	Steven Teig & Asmus Hetzel	01-31-02	
	11.	10/062,047	Steven Teig & Asmuz Hetzel	01-31-02	

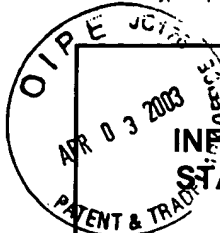
U.S. PATENT DOCUMENTS						
Examiner* Initials	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
	12.	5,649,165		Jain et al.	07-15-97	
	13.	6,301,687	B1	Jain et al.	10-09-01	

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				Filing Date	1/31/02
				First Named Inv ntor	Steven Teig, et al.
				Group Art Unit	2825
				Examiner Name	
Sheet	2	of	2	Attorney Docket Number	SPLX.P0099

### OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner* Initials	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>6</sup>
	14.	D. Jongeneel, R. Otten, Y. Watanabe and R. K. Brayton, Area and Search Space Control for Technology Mapping, 37 <sup>th</sup> Design Automation Conference, 86-91, 2000.	
	15.	Henrik Reif Andersen, An Introduction to Binary Decision Diagrams, October 1997 (minor revisions April 1998), 36 pp.	
	16.	Jerry Burch and David Long, Efficient Boolean Function Matching, Proc. ICCAD 1992, 408-411.	
	17.	John Fishburn, A Depth-Decreasing Heuristic for Combinational Logic; or How to Convert Ripple-Carry Adder into a Carry-Lookahead Adder or Anything In-Between, Proceedings of the 27 <sup>th</sup> Design Automation Conference, 361-364, 1990.	
	18.	Kamal Chaudhary and Massoud Pedram, A Near Optimal Algorithm for Technology Mapping Minimizing Area under Delay Constraints, Proceedings of the 29 <sup>th</sup> Design Automation Conference, 492-498, 1992.	
	19.	Kurt Keutzer, DAGON: Technology Binding and Local Optimization by DAG Matching, Proceedings of the 24 <sup>th</sup> Design Automation Conference, 341-347, 1987	
	20.	Randal Bryant, Symbolic Boolean Manipulation with Ordered Binary Decision Diagrams, CMU CS Tech Report CMU-CS-92-160.	
	21.	Uwe Hinsberger and Reiner Kolla, Boolean Matching for Large Libraries, Proceedings of the 35 <sup>th</sup> Design Automation Conference, 206-211, June 1998.	
	22.	Yuji Kukimoto, Robert K. Brayton, and Prashant Sawkar. Delay-Optimal Technology Mapping by DAG Covering, Dept. of Electrical Engineering and Computer Science, University of California, Berkeley, Strategic CAD Laboratories, Intel Corp., October 1997.	
	23.	Zbigniew J. Czech, et al., An Optimal Algorithm for Generating Minimal Perfect Hashing Functions, Information Processing Letters, 43(5); 257-264, October 1992	

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